

FIG.3

Example of allocated bit rates and Tx power for the DSCH.

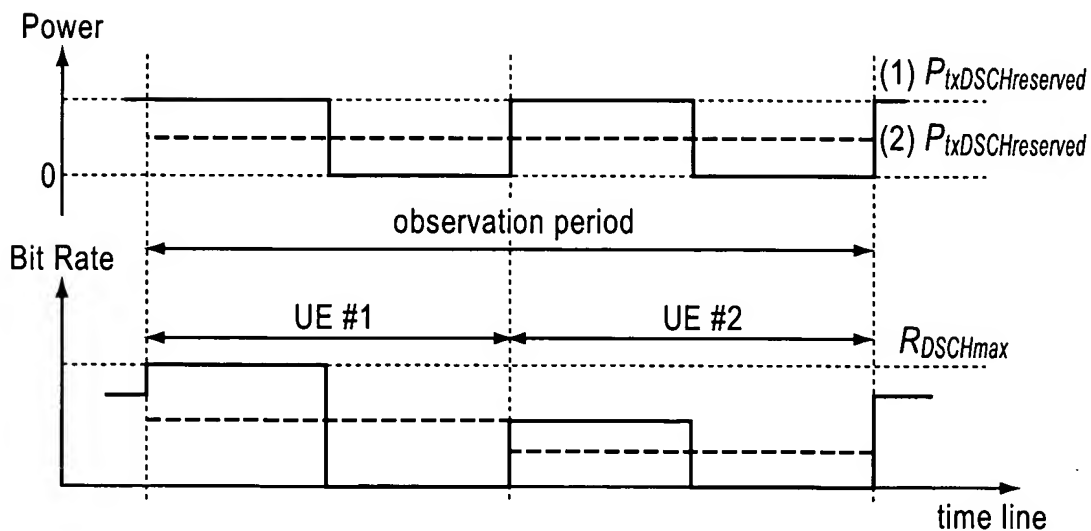


FIG.4

Examples of the DSCH behaviour before and after adjustment of the reserved Tx power level.